

REMARKS

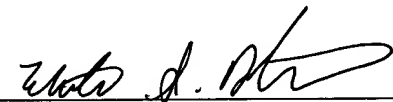
The claims now pending in the Application are claims 1-14 and 17. Claims 15, 16, and 18-20 have been cancelled without prejudice. Claims 1 and 17 have been amended. In response to the restriction requirement set forth in the September 19, 2002 Official Action, Applicants have amended independent claim 1 to recite a circuit for control of an output current in a multiple unit cell array. To this end, claim 1 has been amended to include the array of unit cells and return electrode features found in cancelled claim 15. In light of the amendments made to claim 1, claims 15, 16, and 18-20 have been cancelled. Claim 17 remains in the Application, its dependency being changed to claim 1 from claim 15.

In light of the amendments made herein the need to restrict multiple inventions is now moot. Applicants have elected to prosecute, without traverse, claims falling into the Group II category identified in the September 19, 2002 Office Action. Applicants respectfully request examination of the claims at the Examiner's earliest convenience.

Respectfully submitted,

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

IN THE CLAIMS

Please cancel claims 15, 16, and 18-20 without prejudice.

Please amend claims 1 and 17 as follows:

1. (Amended) A circuit for control of an output current in ~~an active biological control reaction system~~ a multiple unit cell array, comprising:
an array of unit cells arranged in rows and columns, wherein each unit cell comprises:

a first column select transistor, the first column select transistor being adapted for control by a column selector,

a first row select transistor, the first row select transistor being adapted for control by a row selector, the first select transistors being connected in series to each other and between a node and a first supply,

an output connected to the node,

a second column select transistor, the second column select transistor being adapted for control by a column selector, and

a second row select transistor, the second row select transistor being adapted for control by a row selector, the second select transistors being connected in series to each other and between the node and a second supply, and

a return electrode.

17. (Amended) The circuit of claim 15 1 wherein the return electrode is
another unit cell.